

Appl. No. 10/520,340  
Amendment and/or Response  
Reply to Office action of 23 October 2006

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**Amendments to the Specification:**

Please replace the Abstract of the invention with the following rewritten Abstract:

An erasable and programmable non-volatile cell, comprising [[-]] a first transistor having a source, a drain and a gate; [[-]] a floating capacitor having a floating gate and a control gate, said floating gate being connected to said gate of said first transistor; and [[-]] means to detect the state, whether erased or programmed, of the cell; is characterized in that said means to detect the state of the cell comprises a second transistor having a source, a drain and a gate, said second transistor being complementary to said first transistor and said gate of said second transistor being connected to said floating gate.

Atty. Docket No. CH-020023